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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**  
Patent Application

Inventor(s): Warren Snyder, Craig Nemecek and Bert Sullam

Application No.: 09/975,030

Group Art Unit: 2123

Filed: 10/10/01

Examiner: Dwin M. Craig

Title: EMULATOR CHIP-BOARD ARCHITECTURE AND INTERFACE

Form 1449**U.S. Patent Documents**

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
<i>Dme</i>	A	6,564,179	05/13/03	Belhaj	703	26	07/26/99
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						
	J						
	K						

**Foreign Patent or Published Foreign Patent Application**

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	L							
	M							
	N							
	O							
	P							
	Q							

**Other Documents**

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	R	
	S	
	T	
Examiner <i>Dwin</i>	Date Considered <i>10-13-05</i>	

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered.  
Include copy of this form with next communication to applicant.